

Certificate

in

Verification using SystemVerilog

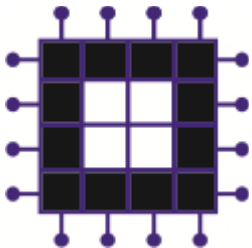
A Corporate Member of FITT-IIT Delhi

**An Initiative by Industry Experts from Cadence,
Atrenta & Patni with qualification from IITs and
BITS-Pilani**

Technology Partners of Cadence Design Systems,

Questa Vanguard Partner of Mentor Graphics,

HEP Partner of Mentor Graphics



DKOP Labs Pvt. Ltd.

Knowledge, Operations and Practices

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VERIFICATION USING SYSTEMVERILOG

MODULES

1. All about verification
2. Data types, operators
3. Procedural statements, tasks and Functions
4. Hierarchy and connectivity
5. Interfaces
6. Object oriented paradigm (OOPs)
7. Programs and clocking blocks
8. Inter-process communication
9. Randomization
10. System Verilog assertions
11. Functional coverage

VERIFICATION LABS

There will be a lot of hands on labs based on the concepts learned in the modules above.

PROJECT WORK

1. Project Study
2. Design & Implementation using QuestaSim or other simulator environment
3. Presentation
4. Document submission & Evaluation of Project

PROGRAM DETAILS

Duration : 100 contact hours

Fees* : Rs 25,000/- plus service tax as applicable

* Additional Rs 7,000 plus service tax for UVM

Payment Terms:

- Rs 5000/- plus service tax at the time of registration
- Balance fee to be paid in the first week of joining

- Payment can be done by DD/Cheque in favor of “DKOP Labs Pvt Ltd” payable at Noida

TOOLS

Our labs are equipped with Licenses of State-of-the-art Mentor Graphics EDA Tools. QuestaSim will be used for this program.

BENEFITS FOR YOU

1. Helps you in understanding the **practical** and **industrial applications** of academic curriculum
2. Build your knowledge to develop **innovative projects** during your **final year** of engineering
3. Enhances the Skill-Set in your resume for **better placement prospects** within the **semiconductor industry**
4. Helps the aspirants of **higher studies abroad** to face the stiff competition from students of other countries
5. Build your confidence through **hands on exposure** to various **tools & technologies**

TEAM OF TRAINERS

DKOP Labs is proud to have highly qualified and experienced professionals from Industry, Research and Academics. For details, [click here](#).

PLACEMENTS

We have been providing excellent placement platform to our trainees in companies like Cadence, Xilinx, ST Microelectronics, Samsung, Synopsys, Mentor Graphics, SmartPlay, TrueChip, Agnisisys, DKOP Labs, etc. For detailed list, [click here](#).